# T.Y. B. Tech (Eelectronics and Telecommunication Engineering) MINIPROJECT

Revised Syllabus of T. Y. B. Tech (E &TC) w. e. f. Academic Year 2020-21

#### Semester V

Sr.	Code No.	Subject	Semester	Credits
No	PCC-ETC501	Signal and Systems	5	5
1.	PCC-ETC502	Electromagnetic Engineering	5	4
2.	PCC-ETC503	Digital and VLSI Design	5	5
3.	PCC-ETC504	Optical Communication	5	5
T.	OEC-ETC501	Open Elective – I	5	4
5.	DOC ETCS05	Simulation and Modeling	5	2
6.	FCC-ETC305	Total		25

#### Semester VI

Sr.	Code No.	Subject	Semester	Credits
No 1	PCC-ETC601	Digital Signal Processing	6	5
2	PCC-ETC602	Microprocessor and Microcontrollers	6	5
3	PCC-ETC603	Power Electronics	6	5
4	PCC-ETC604	Antenna and Wave Propagation	6	5
5	OEC-ETC601	Open Elective – II	6	4
5.	PCC-ETC605	Mini Project	6	1
0.		Total		25

#### > For Theory CIE 30 marks,

- Two tests of 30 marks at college should be conducted and best of two marks should be communicated to university.
- Guidelines to paper setter:

In theory ESE examination of 70 marks following pointes should be considered,

Q.1 MCQ's based on complete syllabus. (Carries 14 Marks)

Q.2 based on unit no 1, 2, 3 (Carries 14 Marks)

Q.3 based on unit no 1, 2, 3 (Carries 14 Marks)

Q.4 based on unit no 4, 5, 6 (Carries 14 Marks)

Q.5 based on unit no 4, 5, 6 (Carries 14 Marks)

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1	PCC- ETC501	4	4	4	1		1	1	-	-	-		CIE ESE	30 70	100	12 28		-	-	2	25	10
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3	PCC- ETC503	4	4	4	-		-	-	1	2	2		CIE ESE	30 70	100	12 28	Guid	50	20	2	25	10
4	PCC- ETC504	4	4	4	-		-	-	1	2	2		CIE ESE	30 70	100	12 28	BOS	50	20	2	25	10
5	OEC- ETC501	3	3	3	1	/1	1	1	-	-	-		CIE ESE	30 70	100	12 28	As per	-	-	2	25	10
6	PCC- ETC505	1	1	1				-	1	2	2							50	20	2	25	10
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2	PCC- ETC602	4	4	4	-	-	-	-	1	2	2		CIE ESE	30 70	100	12 28	elines	50	20	2	25	10
3	PCC- ETC603	4	4	4	-	-	-	•	1	2	2		CIE ESE	30 70	100	12 28	Guid	-	-	2	25	10
4	PCC- FTC604	4	4	4	- 4	-	-	•	1	2	2		CIE ESE	30 70	100	12 28	BOS	50	20	2	25	10
5	OEC-	3	3	3	1	1	1		-	-	-		CIE ESE	30 70	100	12 28	As per	-	-	2	25	10
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# Revised Syllabus of T. Y. B. Tech (E &TC) w. e. f. Academic Year 2020-21

Third Year ELECTRONICS & TELECOMMUNICATION ENGINEERING - CBCS

CIE- Continuous Internal Evaluation

ESE - End Semester Examination

### Note:

PATTERN

- 1. **PCC-ETC:** Professional Core course –Electronics & Telecommunication Engineering are compulsory.
- 2. OCE-ETC: Open Elective Course Electronics & Telecommunication Engineering:
- 3. Winter/Summer Internship/Industrial Training of minimum 15 day's compulsory and evaluation of the same will be carried out in Final year Project Phase internal assessment by respective Guide

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## SHIVAJI UNIVERSITY, KOLHAPUR

# ELECTRONICS & TELECOMMUNICATION ENGINEERING SUBJECT NAME: MINI PROJECT

#### **Course Details**

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Class	T. Y. B. Tech. Sem - VI
Course Code and Course Title Prerequisites Teaching scheme : Practical Credits	PCC-ETC605: Mini Project
Course Code and Course Title Prerequisites Teaching scheme : Practical Credits	Basics of Electronics
Teaching scheme : Practical	2 Hrs.
Credits	1
Evaluation Scheme	

Teaching scheme	Examination scheme
Practical : 2 Hrs. / Week	OE: 50 Marks
	TW: 25 Marks

Cour The	course aims to :
1	Provide students for knowledge of Electronics Components and soldering techniques and its package information for electronics circuit design
2	Provide students for knowledge of the assembling of electronics circuit with components on PCB (Printed Circuit Board) of circuit design.
3	Design and development of Small electronic project based on hardware and software for electronics systems.

Course	Outcomes:
Upon su	ccessful completion of this course, the students will be able to:
1	Practice acquired knowledge within the chosen area of technology for project development.
2	Identify, discuss and justify the technical aspects of the chosen project with a

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# Revised Syllabus of T. Y. B. Tech (E &TC) w. e. f. Academic Year 2020-21

-	Comprehensive and systematic approach.
3	Reproduce, improve and refine technical aspects for engineering projects
4	Work as an individual or in a team in development of technical projects.
5	Communicate and report effectively project related activities and findings.

# Mini project work should consist of following steps.

- 1. Students should propose project ideas & finalize the project idea in consultation with guide.
- 2. Students should submit implementation plan in the form of PERT/CPM chart. This will cover weekly activity of project report.
- 3. Problem definition and specification development in the form of synopsis.
- Design of circuit with calculation & should include a) Analog part b) digital part c) Power supply d) Test strategy if firmware is required produce flow chart.
- 5. Simulation of design using tools like OrCAD, Matlab, etc.
- 6. Design of enclosure & PCB.
- 7. Fabrication & assembly of PCB & enclosure.
- 8. Testing & calibration.
- 9. Measurement of specifications.

#### Note:-

- 1. Project report should include report of all above steps and conclusion.
- 2. Project group should demonstrate and deliver seminar on project.
- 3. A mini project should not exceed three students per group.

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